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M. Cole



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

JOSEPH T. EVANS, JR. ET AL

For: NON-VOLATILE MEMORY CIRCUIT USING
FERROELECTRIC CAPACITOR STORAGE ELEMENT

PRIOR APPLICATION:

Serial No: 440,434
Filed: 11/20/89
Group: 233

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M. Walke

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

(Typed or printed name of person mailing paper or fee)
M. Walke
(Signature of person mailing paper or fee)

Dear Sir:

PRELIMINARY AMENDMENT

Prior to initial review, please amend as follows the accompanying Rule 1.60 continuation application of pending prior Application Serial No. 440,434, filed on November 20, 1989.

IN THE SPECIFICATION

Page 7, line 8, delete "and".
Page 7, line 12, delete "." and substitute --, and --.
Page 7, line 13, add --Figure 17 is an alternate

B' configuration for the circuit shown in Figure 2--.

Page 10, line 8, after "reversed." add --An alternate
circuit configuration 20' illustrating this reversal is shown in
B2 Figure 17. The circuit in Figure 17 is essentially the same as
that shown in Figure 2, but with the bit and drive lines
interchanged and the word line made perpendicular--.